

# PATENT ABSTRACTS OF JAPAN

(11)Publication number : 62-002740

(43)Date of publication of application : 08.01.1987

(51)Int.Cl.

H04L 11/00

(21)Application number : 60-142196

(71)Applicant : TOSHIBA CORP

(22)Date of filing : 28.06.1985

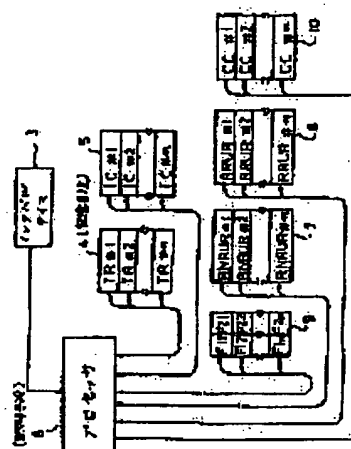
(72)Inventor : TERASAWA MICHIOYUKI

## (54) POLLING AND SELECTING SYSTEM

### (57)Abstract:

**PURPOSE:** To always secure the best polling/selecting efficiency of a master station to a slave station by increasing and decreasing the polling/selecting cycle when the slave station is busy and idle respectively.

**CONSTITUTION:** A processor 6 compares the contents of an RRUR#1 of a register 8 with those of a CC#1 of a counter 10 and changes the value set at a TR#1 of a time register 4 after deciding that an answer of RR is received from a specific slave station up to the upper limit value of the continuous answer times of the RR when the coincidence is obtained from said comparison. In other words, the polling/selecting cycle set value which is presently set to the TR#1 of the register 4 is equal to or little larger than the value of the highest efficiency. Therefore, the value obtained by subtracting a fixed amount of value from the present value of the TR#1 of the register 4 is set again. At the same time, the CC#1 of the counter 10 is reset. As a result, the subsequent polling/selecting cycles are accelerated to the slave station.



## LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

BEST AVAILABLE COPY

Partial Translation of JP62-2740

(19) Japanese Patent Office (JP)

(12) Unexamined Patent Publication Bulletin

(11) Unexamined Patent Publication No. 62-2740

(43) Publication of application: January 8, 1987

(51) Int. Cl6.: H04L 11/00

(54) TITLE OF THE INVENTION :

POLLING/SELECTING METHOD

(21) Patent Application No. 60-142196

(22) Date of Application : June 28, 1985

(72) Inventor : (Michiye Terasawa)

(71) Applicant: Toshiba Corporation

(74) Attorney: (Takashi Honda, et al.)

---

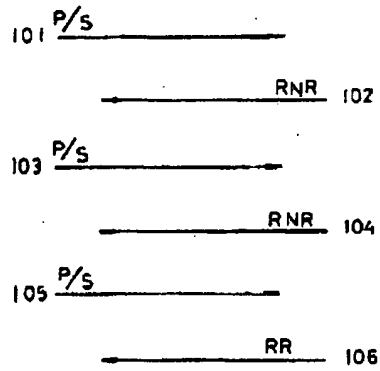
(p 309, left column, line 32 through line 40)

At 101 in Fig. 2, a polling/selecting dealing with a particular event is performed, and when a slave station is in a busy state at the time, RNR is returned to a master station at 102. Then, a polling/selecting is performed again at 103, the slave station is still in a busy state, and therefore, RNR is returned at 104. When a polling/selecting is performed again at 105, a busy state of the slave station is canceled this time, and RR is returned to the master station at 106.

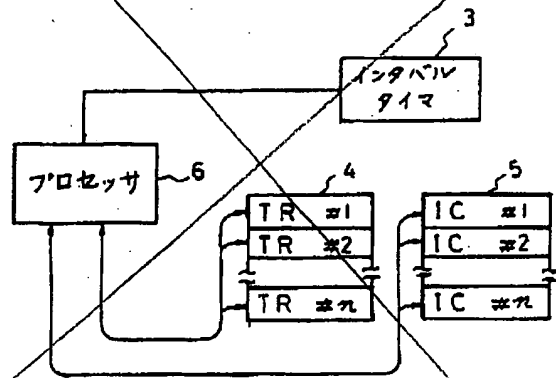
BEST AVAILABLE COPY

(relevant part cited in the Japanese Office Action)

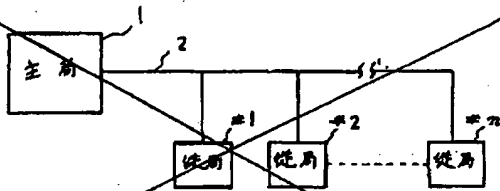
特開昭 62-2710 (8)



第 2 図  
FIG. 2



第 4 図



第 3 図